

Project

[+] *New Component...*

[+] System

Library

[+] Basic Functions

[+] DSP

[+] Interface Protocols

[+] Low Power

[+] Memory Interfaces and Controllers

[+] Processors and Peripherals

[+] Qsys Interconnect

[+] Tri-State Components

[+] University Program

[+] Audio & Video

[+] Bridges

● Avalon to External Bus Bridge

● External Bus to Avalon Bridge

[+] Clock

● Audio Clock for DE-series Boards

● Audio LR Clock for DE-series Boards

● System and SDRAM Clocks for DE-series Boards

● Video Clocks for DE-series Boards

[+] Communications

[+] Generic IO

● Accelerometer SPI Mode

● ADC Controller for DE-series Boards

● Parallel Port

● PS/2 Controller

● USB Controller

[+] Memory

● SRAM Controller

● SSRAM Controller

New...

Edit...

+ Add...