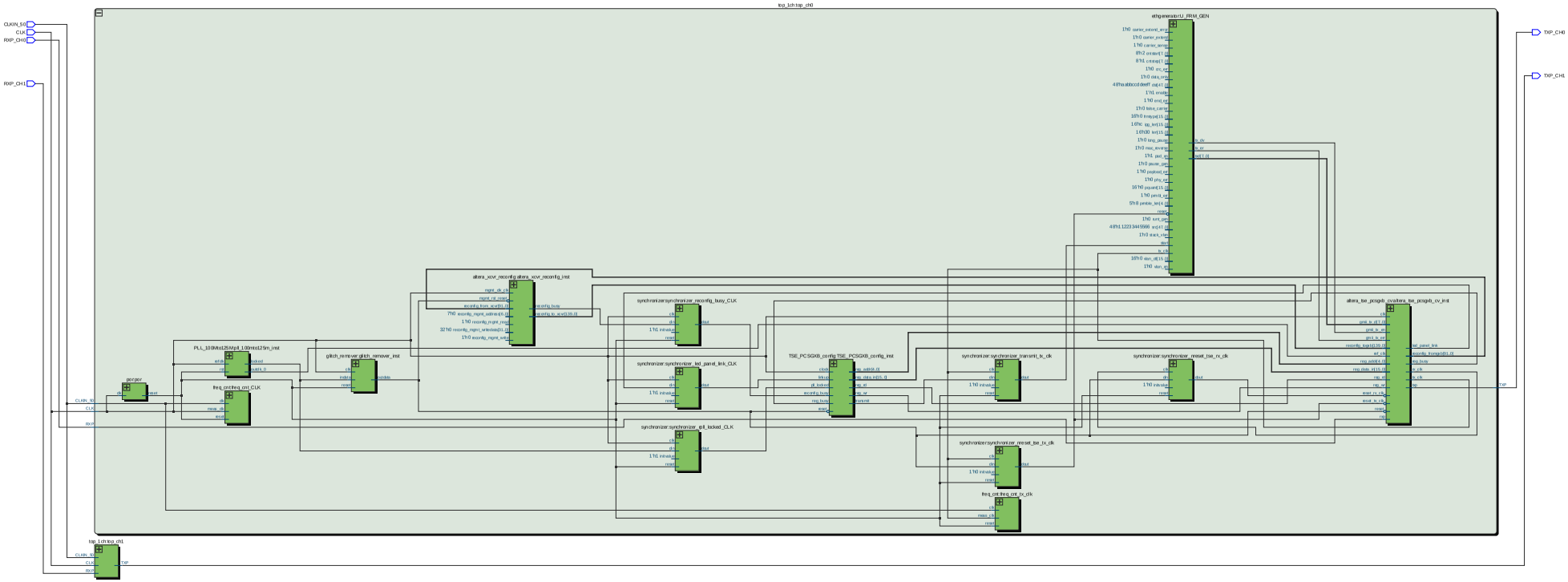
top_1ch:top_ch0
CLKIN_50
CLK
RXP_CH0
RXP_CH1
TXP_CH0
TXP_CH1
ethgeneratorU_FRM_GEN
altera_xcvr_reconfig:altera_xcvr_reconfig_inst
synchronizer:synchronizer_reconfig_busy_CLK
synchronizer:synchronizer_led_panel_link_CLK
synchronizer:synchronizer_pll_locked_CLK
TSE_PCSGXB_config:TSE_PCSGXB_config_inst
synchronizer:synchronizer_transmit_tx_clk
synchronizer:synchronizer_mreset_tse_rx_clk
synchronizer:synchronizer_mreset_tse_tx_clk
freq_cnt:freq_cnt_tx_clk
altera_tse_pcsgxb_cv:altera_tse_pcsgxb_cv_inst
PLL_100Mto125Mpll_100Mto125m_inst
glitch_remover:glitch_remover_inst
freq_cnt:freq_cnt_CLK
porpor
top_1ch:top_ch1