

Use	Connections	Name	Description	Export	Clock	Base	End	IRQ	Tags
<input checked="" type="checkbox"/>		clk	Clock Source						
		clk_in	Clock Input	clk					
		clk_in_reset	Reset Input	reset					
		clk	Clock Output	<i>Click to export</i>	clk				
		clk_reset	Reset Output	<i>Click to export</i>					
<input checked="" type="checkbox"/>		system_pll	Avalon ALTPLL						
		inclk_interface	Clock Input	<i>Click to export</i>	clk				
		inclk_interface_reset	Reset Input	<i>Click to export</i>	[inclk_interface]				
		pll_slave	Avalon Memory Mapped Slave	<i>Click to export</i>	[inclk_interface]	0x0800f000	0x0800f00f		
		c0	Clock Output	<i>Click to export</i>	system_clk				
		c1	Clock Output	<i>Click to export</i>	flash_clk				
		areset_conduit	Conduit	<i>Click to export</i>					
		locked_conduit	Conduit	<i>Click to export</i>					
		phasedone_conduit	Conduit	<i>Click to export</i>					
<input checked="" type="checkbox"/>		cpu	Nios II Processor						
		clk	Clock Input	<i>Click to export</i>	system_clk				
		reset_n	Reset Input	<i>Click to export</i>	[clk]				
		data_master	Avalon Memory Mapped Master	<i>Click to export</i>	[clk]			IRQ 0	
		instruction_master	Avalon Memory Mapped Master	<i>Click to export</i>	[clk]			IRQ 31	
		jtag_debug_module_reset	Reset Output	<i>Click to export</i>	[clk]				
		jtag_debug_module	Avalon Memory Mapped Slave	<i>Click to export</i>	[clk]	0x08000800	0x08000fff		
		custom_instruction_master	Custom Instruction Master	<i>Click to export</i>	[clk]				
<input type="checkbox"/>		tightly_coupled_instruction_memory	On-Chip Memory (RAM or ROM)						
		clk1	Clock Input	<i>Click to export</i>	unconnected				
		s1	Avalon Memory Mapped Slave	<i>Click to export</i>	[clk1]				
		reset1	Reset Input	<i>Click to export</i>	[clk1]				
		s2	Avalon Memory Mapped Slave	<i>Click to export</i>	[clk2]				
		clk2	Clock Input	<i>Click to export</i>	unconnected				
		reset2	Reset Input	<i>Click to export</i>	[clk2]				
<input checked="" type="checkbox"/>		ext_flash	Generic Tri-State Controller						
		clk	Clock Input	<i>Click to export</i>	system_clk				
		reset	Reset Input	<i>Click to export</i>	[clk]				
		uas	Avalon Memory Mapped Slave	<i>Click to export</i>	[clk]	0x00000000	0x007fffff		
		tcm	Tristate Conduit Master	<i>Click to export</i>	[clk]				
<input checked="" type="checkbox"/>		sram	Generic Tri-State Controller						
		clk	Clock Input	<i>Click to export</i>	system_clk				
		reset	Reset Input	<i>Click to export</i>	[clk]				
		uas	Avalon Memory Mapped Slave	<i>Click to export</i>	[clk]	0x01000000	0x0107ffff		
		tcm	Tristate Conduit Master	<i>Click to export</i>	[clk]				
<input checked="" type="checkbox"/>		flash_sram_pin_sharer	Tri-State Conduit Pin Sharer						
		clk	Clock Input	<i>Click to export</i>	system_clk				
		reset	Reset Input	<i>Click to export</i>	[clk]				
		tcm	Tristate Conduit Master	<i>Click to export</i>	[clk]				
		tcs0	Tristate Conduit Slave	<i>Click to export</i>	[clk]				
		tcs1	Tristate Conduit Slave	<i>Click to export</i>	[clk]				
<input checked="" type="checkbox"/>		flash_sram_tristate_bridge	Tri-State Conduit Bridge						
		clk	Clock Input	<i>Click to export</i>	system_clk				
		reset	Reset Input	<i>Click to export</i>	[clk]				
		tcs	Tristate Conduit Slave	<i>Click to export</i>	[clk]				
		out	Conduit	<i>Click to export</i>	memory				
<input checked="" type="checkbox"/>		sysid	System ID Peripheral						
		clk	Clock Input	<i>Click to export</i>	system_clk				
		reset	Reset Input	<i>Click to export</i>	[clk]				
		control_slave	Avalon Memory Mapped Slave	<i>Click to export</i>	[clk]	0x0800d000	0x0800d007		
<input checked="" type="checkbox"/>		peripheral_subsystem	peripheral_system						
		pio_id_eeeprom_dat_external_connection	Conduit Endpoint	pio_id_eeeprom_dat					
		peripheral_subsys_reset_in	Reset Input	<i>Click to export</i>					
		led_pio_external_connection	Conduit Endpoint	led_pio					
		button_pio_external_connection	Conduit Endpoint	button_pio					
		peripheral_subsys_clk_in	Clock Input	<i>Click to export</i>	system_clk				
		pio_id_eeeprom_scl_external_connection	Conduit Endpoint	pio_id_eepron_scl					
		peripheral_bridge_s0	Avalon Memory Mapped Slave	<i>Click to export</i>	[peripheral_subsys_...	0x08000000	0x080000ff		
<input checked="" type="checkbox"/>		ethernet_subsystem	ethernet_system						
		tse_conduit_connection	Conduit Endpoint	enet					
		sgdma_tx_m_read	Avalon Memory Mapped Master	<i>Click to export</i>	[ethernet_subsys_cl...				
		sgdma_rx_m_write	Avalon Memory Mapped Master	<i>Click to export</i>	[ethernet_subsys_cl...				
		descriptor_memory_s2	Avalon Memory Mapped Slave	<i>Click to export</i>	[ethernet_subsys_cl...	0x0800c000	0x0800cfff		
		ethernet_subsys_clk_in	Clock Input	<i>Click to export</i>	system_clk				
		ethernet_subsys_reset_in	Reset Input	<i>Click to export</i>					
		ethernet_bridge_s0	Avalon Memory Mapped Slave	<i>Click to export</i>	[ethernet_subsys_cl...				