

## SIGNAL TAP ANALYZER

The screenshot displays the SignalTap II Logic Analyzer software interface. At the top, the Instance Manager shows a table of instances:

Instance	Status	LEs	Memory	Small	Medium	Large
auto_sig...	Not running	645 cells	131072 bits	0 blocks	32 blocks	0 blocks

The JTAG Chain Configuration panel on the right shows the hardware as USB-Blaster [USB-0] and the device as @1: EP2560 (0x120930DD). Below this, the main data log window is visible, showing a time axis from -3072 to 12288. The data log window is currently empty, with a single entry for '...nal\_connection\_export[2]' at time 0. The bottom of the window shows the Hierarchy Display and Data Log sections, with 'nios6' selected in the hierarchy and 'auto\_sigtap\_0' in the data log. The Windows taskbar at the bottom indicates the time is 4:03 PM on 2/28/2014.

Gets a logic high value when single switch signal takes.

