## SIGNAL TAP ANALYZER

ance Manager	: 🍡 🔊 🔳	🛄 Ready t	acquire										X TTAG Ch	ain Configurat	ion: JTAG ready	
nce	Status	LEs: 645		: 131072	Small: 3/329	Med	lium: 195/255	Large: 0/2								
🖁 auto_sig		645 cells	131072		0 blocks		locks	0 blocks					Hardwar	e: USB-Blast		Setup
													Device:	@1: EP2S	60 (0x120930DD)	<ul> <li>Scan C</li> </ul>
													>> sc	F Manager:	<u>_</u>	
og: 2014/02/28										sert time bar						10000
/pe Alias	Name .nal_connection_exp	-3072	-2048	-1024	. 0	1024	2048	3072	4096	5120	6144	7168	8192	9216	10240 11264	12288
<u>&gt;  </u>	.nai_connection_exp	Jrųzj														
		•														
Data	Setup	•														
archy Display		~	🔲 Data Log													
🗸 🦈 nios6		×		signaltap_0												
auto_signalt																

Gets a logic high value when single switch signal takes.

Edit Vie	ew Project Processing	Tools Wind	ow <u>H</u> elp 💎						1				
nce Manag	er: 🔌 🔊 🔳 🛃	Program the d	evice to continue					JTAG Chain	Configuration: JTAG ready				
ce		698	Memory: 524288	Small: 63/329	Medium: 272/255	Large: 1/2	. 🕑	_					
		cells	524288 bits	0 blocks	Can't fit 128 block			Hardware:	USB-Blaster [USB-0]	▼ Se			
								Device:	@1: EP2S60 (0x120930DD)	<ul> <li>Sca</li> </ul>			
								>> SOF M	anager: 🛓 🕕				
								1					
rigger: 2014/02/28 16:15:19 #1		Lock mode: Allow all changes						Signal Configuration:					
	Node	Data Enable	Trigger Enable		Conditions		Clo	ck: dk dk					
e Alias	Name	4			2 📝 Basic AND 🔻			ata		-			
_	nal_connection_export[0]		<b>V</b>										
	nal_connection_export[1]						S	ample depth: 12	28 K 🔻 RAM type: M4K/M9k	•			
	nal_connection_export[2]	V V	<ul> <li>✓</li> <li>✓</li> </ul>					Segmented:	2 64K sample segments	v			
	nal_connection_export[3]	<b>V</b>	<b>v</b>					Storage qualifier					
								Type:	Continuous	•			
									o_stp_external_storage_qualifier				
								✓ Record data					
								Disable stora	ige qualifier				
							Т	rigger					
							1	rigger flow contro	ol: Sequential	•			
							1	rigger position:	Pre trigger position	•			
							1	rigger conditions:	2	•			
								Trigger in					
								O Pin:					
								Ø Node:					
								Instance:		~			
Data	a Setup												
chy Disp	lay:	×	Data Log: 🛃										
🗈 nic	ve6		👯 auto_signaltap	0									

## 4 switches assigned,

