Flash programming procedure

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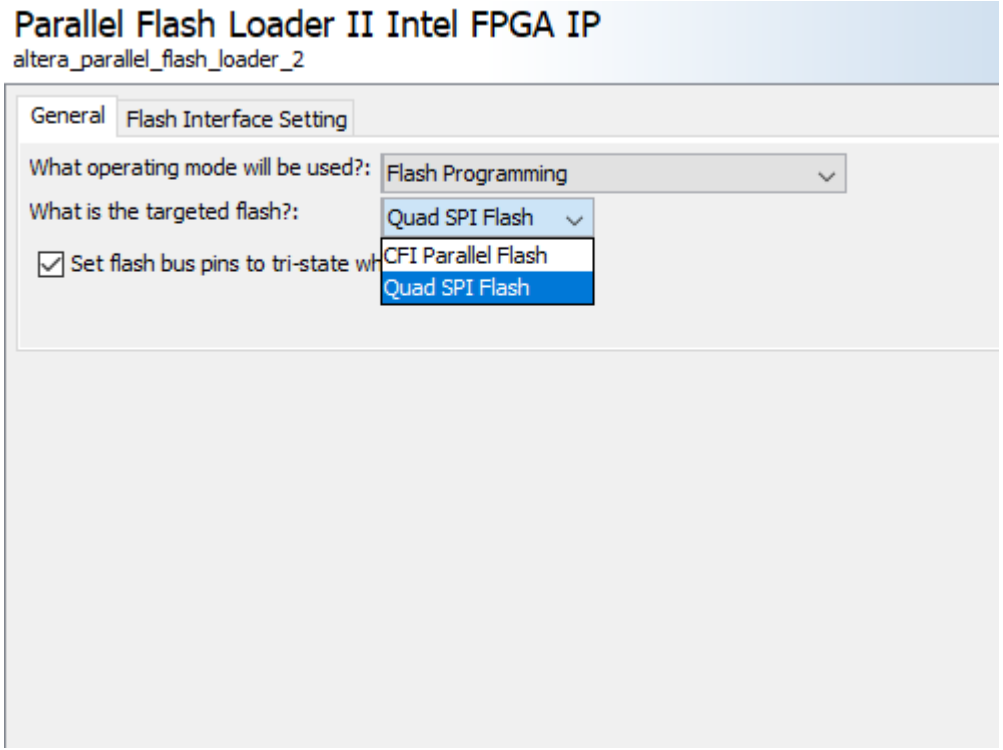
Generating PFL.sof

Parallel Flash Loader example

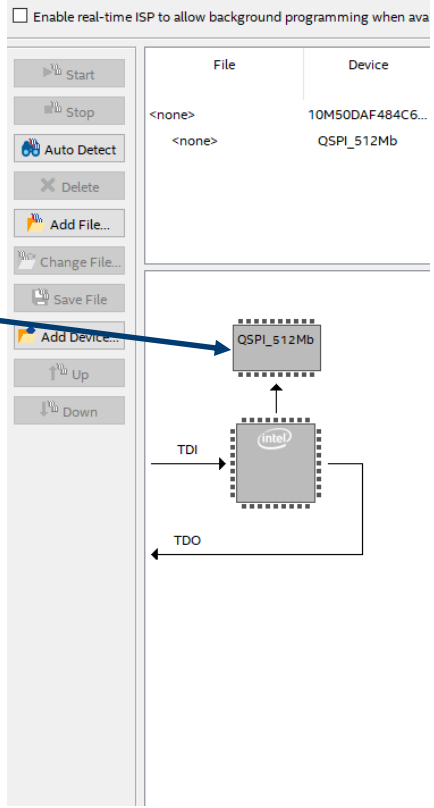
In this section you will generate the PFL project to make the FPGA work as a bridge to the parallel flash, then program the flash with the HEX.pof file.

- Generate the HDL of a Parallel flash loader in Qsys.
- Connect it in the Top_Level in Quartus.
- Compile the project.
- Once compilation successful, open the Quartus Programmer.



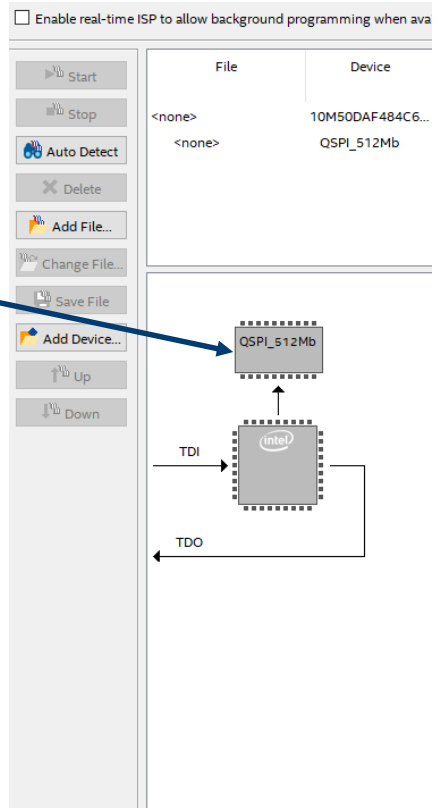
Parallel Flash Loader example

- In Quartus programmer, program the FPGA with the generated pfl.sof
- Press auto detect after a successful programming. (don't turn off the board power)
- The Quartus programmer will detect the parallel flash



Parallel Flash Loader example

- Right click the flash device>> Change file
- Select the generated HEX.pof file that carries the HEX file.
- After this step you can boot your NIOS II and it will find the HEX in place.
- Follow the following steps to boot NIOS II from a serial flash.

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