

Intel_FLASHIPT\ fsm ['machine0']

Global Actions

Package List

LIBRARY ieee;
USE ieee.std_logic_1164.all;
USE ieee.std_logic_arith.all;

Concurrent Statements

clk<=clk_H;

Architecture Declarations

Signals Status

SIGNAL	SCOPE	DEFAULT	RESET	STATUS
A	OUT			COMB
B	OUT			COMB
DQ	OUT			COMB
adv	OUT			COMB
ce	OUT			COMB
clk	OUT			COMB
oe	OUT			COMB
rs	OUT			COMB
we	OUT			COMB
wt	OUT			COMB

State Register Statements

Process Declarations

<company name>		Project:	<enter project name here>
Title:	<enter diagram title here>	<enter comments here>	
Path:	Intel_FLASHIPT\ fsm		
Edited:	by DJ on 23 Dec 2007		

