

	Address	Description	Read/Write	MSM Mode
0	00	Read Master State Machine Current State Mode	R	Factory/Application
1	01	Write the early conf done bit	W	Factory
2	02	Write watchdog timeout value.	W	Factory
3	03	Write watchdog enable.	W	Factory
4	04	Read current Factory mode boot_address / Write boot_address.	R/W	Factory
5	05	N/A	N/A	N/A
6	06	Write the (Osc_int) option bit to allow the internal oscillator as startup state machine clock.	W	Factory
7	07	N/A	N/A	N/A
8	08	Read Past Status 1 MSM mode	R	Factory
9	09	N/A	N/A	N/A
10	0A	Read current Application mode Watch Dog Time Out Value	R	Factory
11	0B	Read current Application mode Watch Dog Enable	R	Application
12	0C	Read Past Status 1 boot_address	R	Application
13	0D	N/A	N/A	N/A
14	0E	N/A	N/A	N/A
15	0F	Read Past Status 1 reconfiguration trigger conditions source	R	Factory
16	10	Read Past Status 2 MSM mode	R	Factory
17	11	N/A	N/A	N/A
18	12	N/A	N/A	N/A
19	13	N/A	N/A	N/A
20	14	Read Past Status 2 boot_address	R	Factory
21	15	N/A	N/A	N/A
22	16	N/A	N/A	N/A
23	17	Read Past Status 2 reconfiguration trigger conditions source	R	Factory
24	18	N/A	N/A	N/A
25	19	Read the early CONF_DONE check (Cd_early) option bit from input register.	R	Factory
26	1A	Read watchdog timeout value from input register.	R	Factory
27	1B	Read watchdog enable value from input register.	R	Factory
28	1C	Read boot_address value from input register.	R	Factory
29	1D	N/A	N/A	N/A
30	1E	Read the Osc_int option bit from input register.	R	Factory