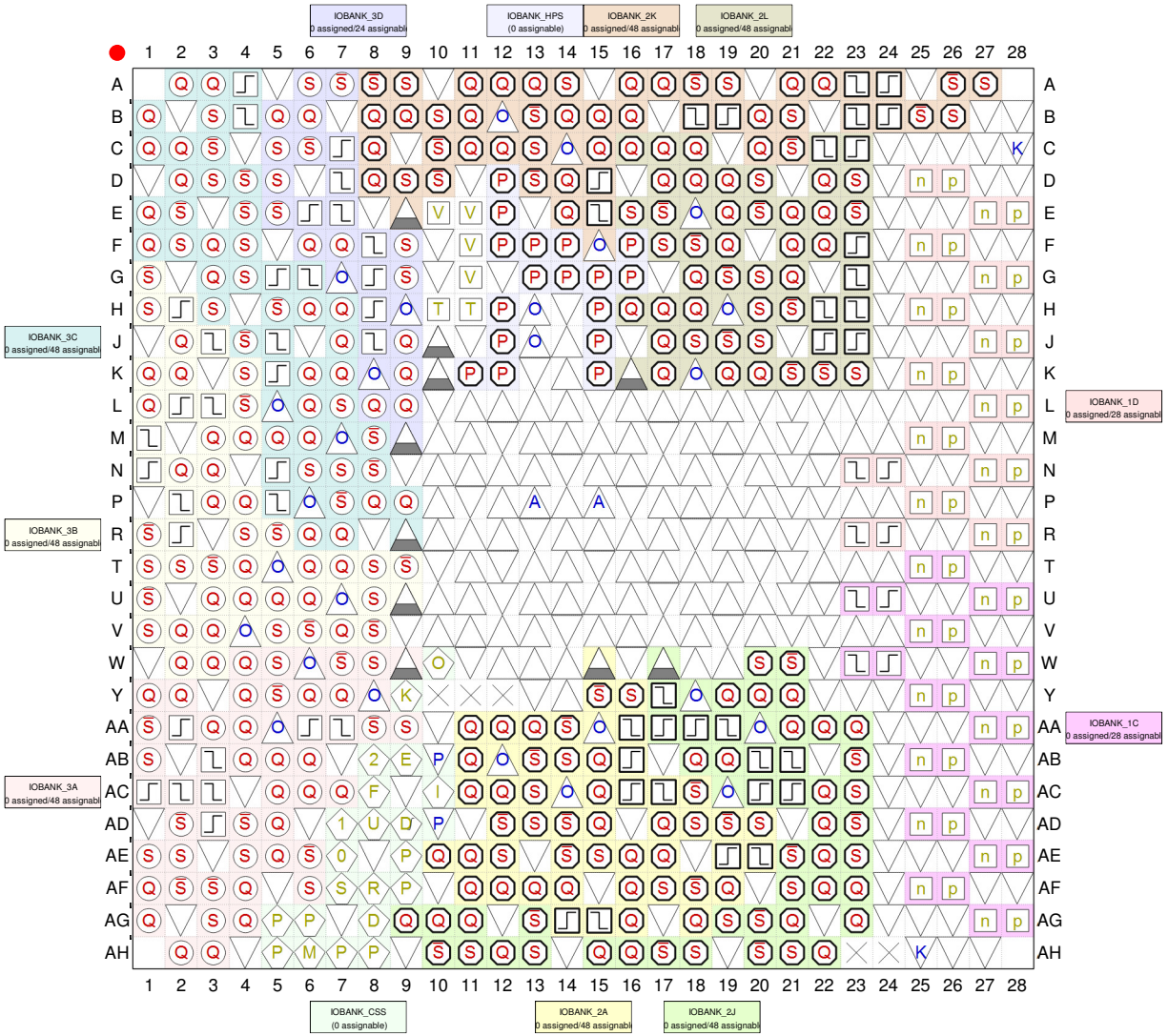


Top View - Flip Chip

Arria 10 - 10AS027E4F29E3SG



Legend:

- | | | | | | | | |
|--|---------------------------|--|-----------------------------|--|---------------------|--|---------------------|
| | User I/O | | User assigned I/O | | Fitter assigned I/O | | Unbonded pad |
| | Reserved pin | | DQ | | DQS | | DQSB |
| | Hard processor system pin | | CLK_n | | CLK_p | | GX_X*n |
| | GX_X*p | | TEMPDIODE | | VSIG | | MSEL0 |
| | MSEL1 | | MSEL2 | | CONF_DONE | | DCLK |
| | nCE | | nCONFIG | | TDI | | TCK |
| | TMS | | TDO | | TRST | | nSTATUS |
| | nIO_PLLUP | | Other dedicated programming | | VREF | | VCCP/VCCR/VCCT |
| | VCCA | | VCCIO | | GND | | GND for programming |
| | RREF | | No connect | | | | |