.JIC Flash programming procedure

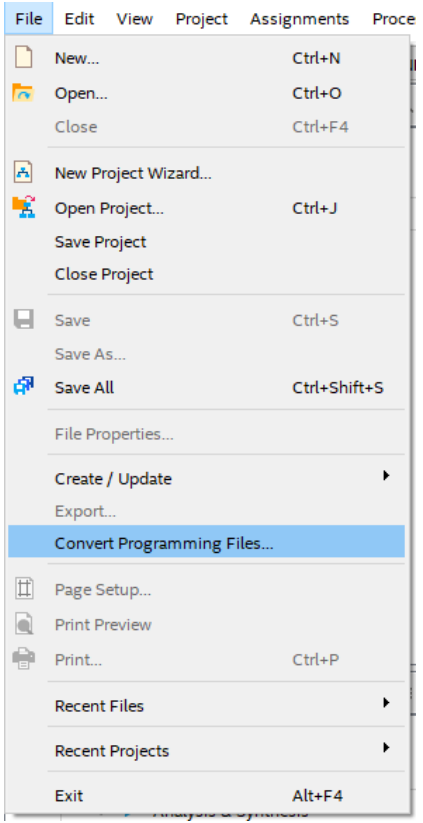
Flash programming procedure

- The following slides show how to create a jic file to program your flash using Quartus programmer.
- The example been created on Quartus 18.1
- The example requires a successful FOS and HEX files.

April 2019

Flash programming procedure

- 1- Go to File>> Convert Programming files

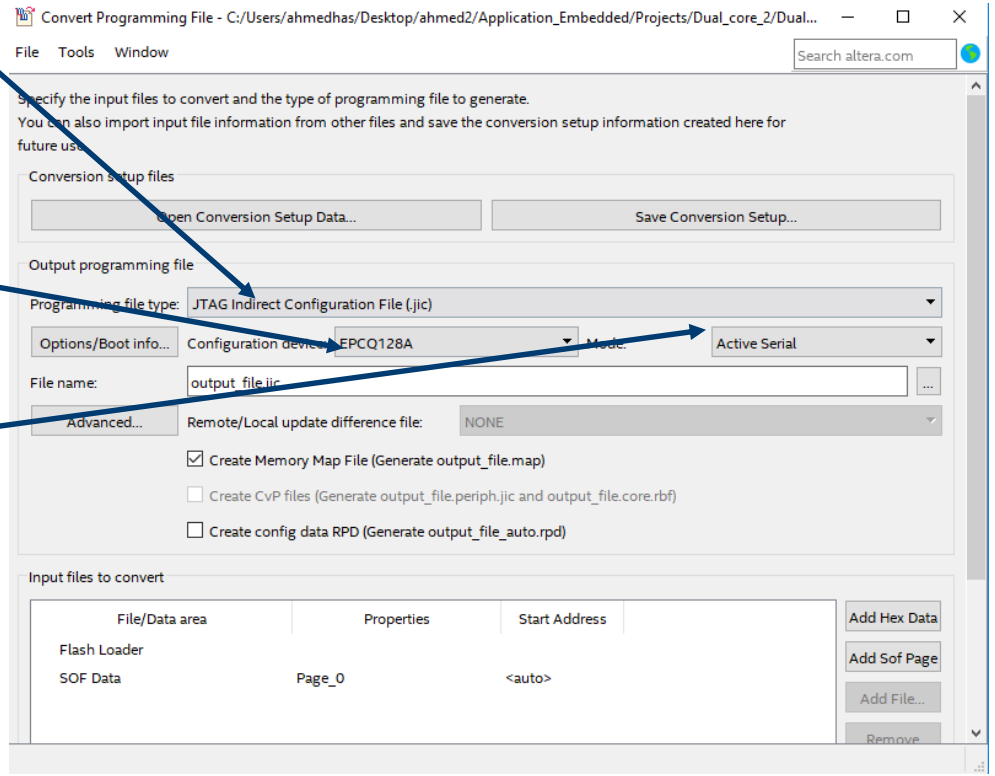


Flash programming procedure

- Select JTAG Indirect Configuration File (.jic)

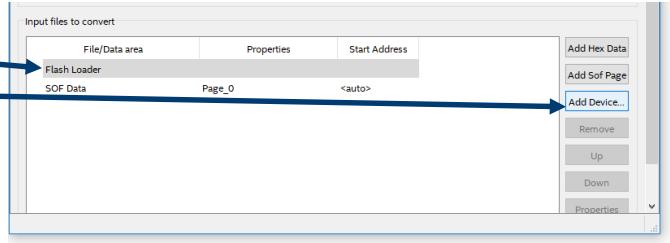
Select your flash device.

- Select Active Serial mode.

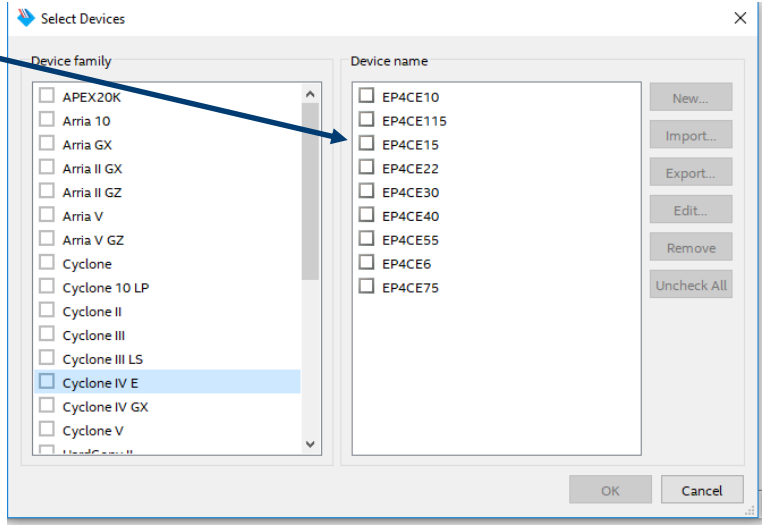


Flash programming procedure

- Select the flash loader page
- Press Add Device.

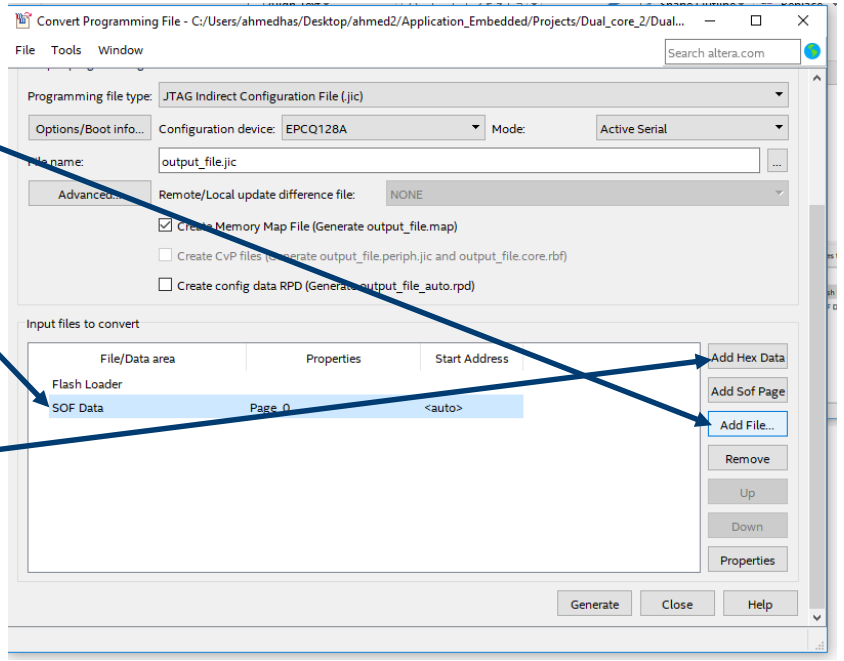


- Select your device.



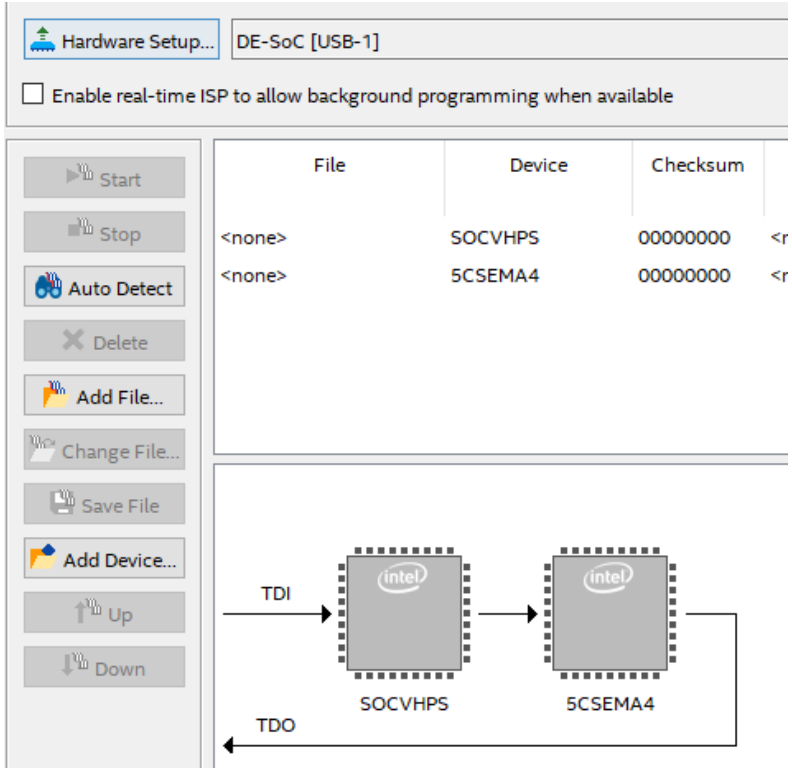
Flash programming procedure

- Select the SOF Data page and Add the SOF file.
- Add HEX Data if you want to include it into the generated (.jic) file.



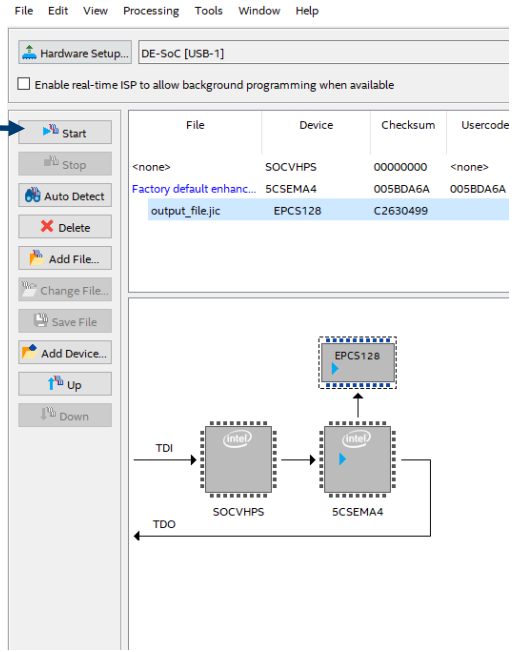
Flash programming procedure

- In the Quartus programmer, select Auto Detect after connecting your board.
- Right click the FPGA device>> Change file.
- Select your (.jic) file.



Flash programming procedure

- The FPGA will auto detect the flash device based on the jic file loaded.
- Press Start to program the flash.

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