

Intel_FLASHIPT\fsim ['machine0']

Signals Status		State Register Statements	
SIGNAL	SCOPE	DEFAULT	RESET
A	OUT		COMB
B	OUT		COMB
DQ	OUT		COMB
adv	OUT		COMB
ce	OUT		COMB
clk	OUT		COMB
oe	OUT		COMB
rs	OUT		COMB
we	OUT		COMB
wt	OUT		COMB

Process Declarations

<company name>		Project:	<enter project name here>
Title:	<enter diagram title here>		
Path:	Intel_FLASHIPT\fsim		
Edited:	by DJ on 25 Dec 2007		

Package List

LIBRARY ieee;
 USE ieee.std_logic_1164.all;
 USE ieee.std_logic_arith.all;

Global Actions

Concurrent Statements

clk<=clk_H;

Architecture Declarations

