

The screenshot shows the Altera Programmer software interface. At the top, the window title is "Programmer - D:/RUNNING/soc\_exps/sopc\_builder\_tutorial/LIGHTS - LIGHTS - [LIGHTS.cdf]\*". The menu bar includes File, Edit, View, Processing, Tools, and Window. Below the menu bar, there is a "Hardware Setup..." button and a dropdown menu showing "USB-Blaster [USB-0]". To the right, the "Mode" is set to "JTAG" and there is a "Progress:" indicator. A checkbox labeled "Enable real-time ISP to allow background programming (for MAX II devices)" is present.

On the left side, there is a vertical toolbar with buttons for Start, Stop, Auto Detect, Delete, Add File..., Change File..., Save File, Add Device..., Up, and Down.

The main area contains a table with the following columns: File, Device, Checksum, Usercode, Program/Configure, Verify, Blank-Check, Examine, Security Bit, Erase, and ISP CLAMP. The table has one row:

File	Device	Checksum	Usercode	Program/Configure	Verify	Blank-Check	Examine	Security Bit	Erase	ISP CLAMP
LIGHTS.sof	EP3C16F484	00ID3CD6	FFFFFFF	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

Below the table is a diagram of an EP3C16F484 device. The device is represented as a square with "ALTERA" in the center and "EP3C16F484" at the bottom. An arrow labeled "TDI" points into the top-left corner, and an arrow labeled "TDO" points out from the bottom-left corner.

At the bottom of the window is a log window with the following text:

```
Info: Started Programmer operation at Tue Jan 03 10:50:52 2012
Info: Configuring device index 1
Info: Device 1 contains JTAG ID code 0x020F20DD
Error: CONF_DONE pin failed to go high in device 1
Error: Operation failed
```