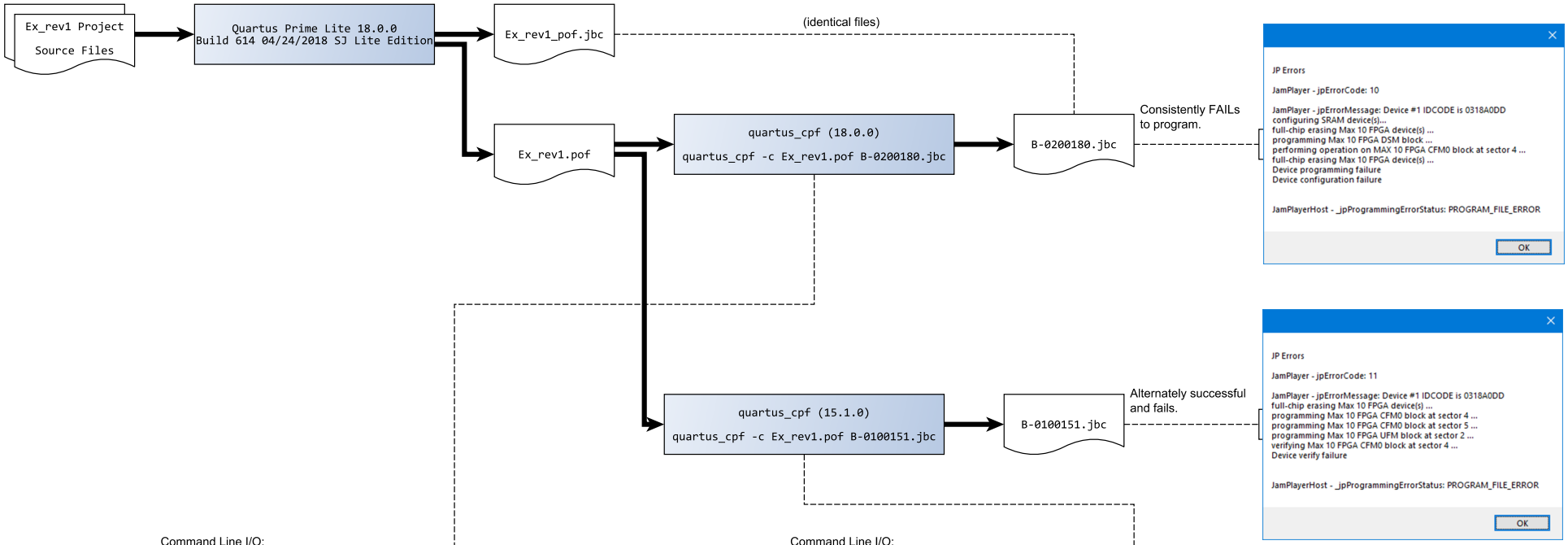

Command Line I/O:

```
c:\intelFPGA_lite\18.0\quartus\bin64>quartus_cpf -c Ex_rev1.pof B-0200180.jbc
Info: *******************************************************************
Info: Running Quartus Prime Convert_programming_file
    Info: Version 18.0.0 Build 614 04/24/2018 SJ Lite Edition
    Info: Copyright (C) 2018  Intel Corporation. All rights reserved.
    Info: Your use of Intel Corporation's design tools, logic functions
    Info: and other software and tools, and its AMPP partner logic
    Info: functions, and any output files from any of the foregoing
    Info: (including device programming or simulation files), and any
    Info: associated documentation or information are expressly subject
    Info: to the terms and conditions of the Intel Program License
    Info: Subscription Agreement, the Intel Quartus Prime License Agreement,
    Info: the Intel FPGA IP License Agreement, or other applicable license
    Info: agreement, including, without limitation, that your use is for
    Info: the sole purpose of programming logic devices manufactured by
    Info: Intel and sold by Intel or its authorized distributors.  Please
    Info: refer to the applicable agreement for further details.
    Info: Processing started: Tue May 14 09:55:05 2019
Info: Command: quartus_cpf -c Ex_rev1.pof B-0200180.jbc
Info (210117): Created JAM or JBC file for the specified chain:
Device 1 (10M04SCE144; Ex_rev1.pof)
Info: Quartus Prime Convert_programming_file was successful. 0 errors, 0 warnings
    Info: Peak virtual memory: 4438 megabytes
    Info: Processing ended: Tue May 14 09:55:06 2019
    Info: Elapsed time: 00:00:01
    Info: Total CPU time (on all processors): 00:00:01

c:\intelFPGA_lite\18.0\quartus\bin64>
```

Command Line I/O:

```
C:\altera_lite\15.1\quartus\bin64>quartus_cpf -c Ex_rev1.pof B-0100151.jbc
Info: *******************************************************************
Info: Running Quartus Prime Convert_programming_file
    Info: Version 15.1.0 Build 185 10/21/2015 SJ Lite Edition
    Info: Copyright (C) 1991-2015 Altera Corporation. All rights reserved.
    Info: Your use of Altera Corporation's design tools, logic functions
    Info: and other software and tools, and its AMPP partner logic
    Info: functions, and any output files from any of the foregoing
    Info: (including device programming or simulation files), and any
    Info: associated documentation or information are expressly subject
    Info: to the terms and conditions of the Altera Program License
    Info: Subscription Agreement, the Altera Quartus Prime License Agreement,
    Info: the Altera MegaCore Function License Agreement, or other
    Info: applicable license agreement, including, without limitation,
    Info: that your use is for the sole purpose of programming logic
    Info: devices manufactured by Altera and sold by Altera or its
    Info: authorized distributors.  Please refer to the applicable
    Info: agreement for further details.
    Info: Processing started: Tue May 14 10:05:58 2019
Info: Command: quartus_cpf -c Ex_rev1.pof B-0100151.jbc
Warning (210120): MAX 10 information is incomplete. The ISP clamp functionality will be disabled.
Info (210117): Created JAM or JBC file for the specified chain:
Device 1 (10M04SCE144; Ex_rev1.pof)
Info: Quartus Prime Convert_programming_file was successful. 0 errors, 1 warning
    Info: Peak virtual memory: 261 megabytes
    Info: Processing ended: Tue May 14 10:06:00 2019
    Info: Elapsed time: 00:00:02
    Info: Total CPU time (on all processors): 00:00:01

C:\altera_lite\15.1\quartus\bin64>
```