

+	E	lpm_fifo_512x16	Entity	C:/Users/H392621/Documents/minigci
+	E	lpm_fifo_512x16_wc	Entity	C:/Data/Projects/Sensor_IF_FPGA/tb/
+	E	lpm_fifo_512x16e	Entity	C:/Users/H392621/Documents/minigci
+	E	lpm_fifo_512x16f	Entity	C:/Data/Projects/Sensor_IF_FPGA/tb/
+	E	lpm_fifo_512x5	Entity	C:/Data/Projects/Sensor_IF_FPGA/tb/
+	E	lpm_fifo_512x8	Entity	C:/Data/Projects/Sensor_IF_FPGA/tb/
+	E	lpm_fifo_8kx6	Entity	C:/Users/H392621/Documents/minigci
+	E	lpm_iobuf	Entity	C:/Users/H392621/Documents/minigci
+	E	lpm_iobuf_iobuf_bidir_9qo	Entity	C:/Users/H392621/Documents/minigci
+	E	lpm_mpy_16x16	Entity	C:/Users/H392621/Documents/minigci
+	E	lpm_mpy_20x20	Entity	C:/Users/H392621/Documents/minigci
+	E	lpm_mpy_24x24	Entity	C:/Users/H392621/Desktop/FPGA WOI
+	E	lpm_smpy_16x16	Entity	C:/Users/H392621/Desktop/FPGA WOI
+	E	lpm_smpy_24x16	Entity	C:/Users/H392621/Documents/minigci
+	E	lpm_smpy_24x17	Entity	C:/Users/H392621/Desktop/FPGA WOI
+	E	lpm_smpy_25x17	Entity	C:/Users/H392621/Documents/minigci

```

33 --refer to the applicable agreement for
34 --https://fpgasoftware.intel.com/eula.
35
36
37 --altiobuf_bidir CBX_AUTO_BLACKBOX="ALL
38 --VERSION_BEGIN 20.1 cbx_altiobuf_bidir
39
40 LIBRARY cyclonev;
41 USE cyclonev.all;
42
43 --synthesis_resources = cyclonev_io_ibu
44 LIBRARY ieee;
45 USE ieee.std_logic_l164.all;
46
47 ENTITY lpm_Iobuf_iobuf_bidir_9qo IS
48 PORT
49 (

```

Transcript

```

questa> vcom -explicit -2002 C:/Users/H392621/Documents/minigci_fpga/lpm/lpm_iobuf.vhd
Questa Intel FPGA Edition-64 vcom 2022.1 Compiler 2022.01 Jan 29 2022
Start time: 09:09:16 on Oct 25,2022
vcom -reportprogress 300 -explicit -2002 C:/Users/H392621/Documents/minigci_fpga/lpm/lpm_Iobuf.vhd
-- Loading package STANDARD
** Error: C:/Users/H392621/Documents/minigci_fpga/lpm/lpm_Iobuf.vhd(40): (vcom-1598) Library "cyclonev" not found.
** Error: C:/Users/H392621/Documents/minigci_fpga/lpm/lpm_Iobuf.vhd(41): (vcom-1136) Unknown identifier "cyclonev".
-- Loading package TEXTIO
-- Loading package std_logic_l164
** Note: C:/Users/H392621/Documents/minigci_fpga/lpm/lpm_Iobuf.vhd(47): VHDL Compiler exiting
End time: 09:09:17 on Oct 25,2022, Elapsed time: 0:00:01
Errors: 2, Warnings: 0
C:/intelFPGA_pro/22.2/questa_fe/win64/vcom failed.

```

