



Intel® Desktop Board DX58S02 Specification Update

November 2010

Order Number: G22441-001US

The Intel® Desktop Board DX58S02 may contain design defects or errors known as errata, which may cause the product to deviate from published specifications. Current characterized errata are documented in this Specification Update.

Revision History

Revision	Revision History	Date
-001	This document is the first Specification Update for the Intel® Desktop Board DX58SO2	November 2010

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The Intel® Desktop Board DG31GL may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

Contact your local Intel sales office or your distributor to obtain the latest specifications before placing your product order.

Copies of documents which have an ordering number and are referenced in this document, or other Intel literature, may be obtained from:

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Specification Update for the Intel[®] Desktop Board DX58SO2

This document is an update to the specifications contained in the *Intel[®] Desktop Board DX58SO2 Technical Product Specification* (Order Number: G13828). It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools. It will contain Specification Changes, Errata, Specification Clarifications, and Documentation Changes.

For specification updates concerning the Intel processor that may apply to this desktop board, refer to the *Intel[®] Core[™] i7 Processor Extreme Edition Series and Intel[®] Core[™] i7 Processor Specification Update* (Order Number: 320836)

Unless otherwise noted in this document, it should be assumed that any processor errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel[®] X58 Express Chipset Specification Update* (Order Number: 320839) for specification updates concerning the X58 IOH Controller and that may apply to the desktop board DX58SO2. Unless otherwise noted in this document, it should be assumed that any IOH errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel[®] I/O Controller Hub 10 (ICH10) Family Specification Update* (Order Number: 319974) for specification updates concerning the 82801GB ICH10 and 82801GR ICH10R I/O Controller Hub and that may apply to the desktop board. Unless otherwise noted in this document, it should be assumed that any ICH10 errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Terminology

Specification Changes are modifications to the current published specifications. These changes will be incorporated in the next release of the specifications.

Errata are design defects or errors. Characterized errata may cause the desktop board behavior to deviate from published specifications. Hardware and software designed to be used with any given Altered Assembly (AA) and BIOS revision level must assume that all errata documented for that AA and BIOS revision level are present on all desktop boards.

Specification Clarifications describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.

Documentation Changes include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.

General Information

Basic Desktop Board DX58SO2 Identification Information

AA Revision	BIOS Revision	Notes
G10925-205	SOX5820J.86A	Initial Production

Notes:

1. The AA number is found on a small label on the component side of the board.
2. The X58 Chipset kit used on this AA revision consists of two components as follows:

Device	Stepping	S-Spec Numbers

Summary of Changes

The following table indicates the Specification Changes, Errata, Specification Clarifications, or Documentation Changes that apply to the Intel® Desktop Board DX58SO2. Intel intends to fix some of the errata in a future revision of the desktop board, and to account for the other outstanding issues through documentation or specification changes as noted.

The following notations are used in the table:

Doc:	Document change or update that will be implemented.
PlanFix:	This erratum may be fixed in a future revision of the desktop board, driver, or BIOS.
Fixed:	This erratum has been previously fixed.
NoFix:	There are no plans to fix this erratum.
Shaded:	This erratum is either new or modified from the previous version of the document.

No.	Plans	Errata
		There is no characterized erratum for this product