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Dear Design Professional,

This letter is intended to outline the memory sources within the Intel® FPGA USB Download Cable II [PL-USB2-BLASTER] and their level of volatility.

The Intel® FPGA USB Download Cable II has four sources of memory:

1. On-board non-volatile firmware storage memory 128kbit (Serial EEPROM, non-volatile, Microchip 24AA128 128K I2C CMOS Serial EEPROM)
2. Cypress EZ-USB FX2LP USB controller (CY7C68013A) volatile memory:
    a. 16kB firmware RAM
    b. 4kB USB endpoint buffering
    c. 8051 register architecture
3. Altera MAX II CPLD (non-volatile part)
    a. User Flash Memory (512x16)
    b. Device configuration
4. Altera MAX II CPLD logic device (volatile part)
    a. Inaccessible registers

The firmware does support programming the non-volatile firmware storage memory as well as the non-volatile UFM. These features are only internally available but the functionality is there. Data can also be written to the USB endpoint buffers, but this data is not retained when powered off. All other firmware RAM is not retained upon power off and is not user accessible.

Should you have any additional questions or require more information, please do not hesitate to contact me.

Best Regards,

Joel Seely
Product Management
Intel Corporation

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